

Shri Sant Gajanan Maharaj College of Engineering, Shegaon

Department of Electronics and Telecommunication Engineering

Date: 18/3/2024

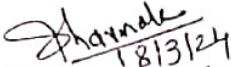
This is notified to all concerned that, as per the SGBAU ordinance No.1 of 2016 (B-4.2), the pre-defense for PhD thesis submission of Mr. Swapnil Panjabrao Badar, PhD scholar of Electronics and Telecommunication Engineering department is scheduled as mentioned below. All interested faculty members are requested to join the same.

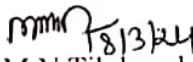
Date: 28th March 2024, Thursday

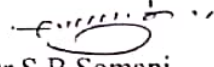
Time: 4 PM

Venue: Seminar Hall, EXTC Dept.

Thesis Title: Optimal Design of Polar Code decoder for 5G Wireless Application and its Performance Evaluation.


Dr. N. S. Dharmale
Coordinator (R&D & PhD Cell)


Dr. M. N. Tibdewal
HoD, EXTC


Dr. S. B. Somani
Principal and Chairperson of RAC

Copy to:

- 1) Dr. S. B. Patil, Member RAC and subject expert for evaluation.
- 2) Dr. R. S. Dhekekar, Member RAC and subject expert for evaluation.
- 3) Dr. K. B. Khanchandani, Supervisor of Mr. Swapnil Panjabrao Badar.
- 4) Information and circulation amongst all faculty members and PhD scholars (EXTC department).
- 4) All HODs for information and circulation amongst faculty members and PhD scholars.
- 5) Mr. A. B. Wagh, for publication on website.