

# Shri Sant Gajanan Maharaj College of Engineering, Shegaon

Date: 27/09/2023

This is notified to all concerned that, as per the **SGBAU ordinance No. 1 of 2016 (B-4.2)**, the **pre-defence** for PhD thesis submission of **Mr. Kamlesh Tulshiram Kahar**, PhD Scholar of Electronics & Telecommunication Engineering Department is scheduled as mentioned below. All interested are requested to join the same.

**Date: Saturday, 7<sup>th</sup> Oct. 2023**

**Time: 09.30 am onwards**

**Venue: Seminar Hall, Electronics & Telecommunication Engineering Department**



Dr. R. S. Dhekekar

Coordinator (R&D & PhD Cell)



Dr. S. B. Somani

Principal and Chairperson of RAC

Copy to:

- 1) Dr. S. B. Patil, Member RAC and Subject Expert for Evaluation
- 2) Dr R. S. Dhekekar, Supervisor of Mr. Kamlesh Tulshiram Kahar
- 3) Head, Electronics & Telecommunication Engineering. Department for information and circulation amongst faculties and PhD Scholars
- 4) All HODs for information and circulation amongst faculties and PhD Scholars
- 5) Mr A B Wagh, for publication on website