

Shri Sant Gajanan Maharaj College of Engineering, Shegaon

Date: 07/06/2023

This is notified to all concerned that, as per the SGBAU ordinance No.1 of 2016 (B-4.2), the pre-defence for PhD thesis submission of Mr. Ravishankar Shaligram Kankale, PhD Scholar of Electrical Engineering Department is scheduled as mentioned below. All interested are requested to join the same.

Date: Saturday, 17th June 2023

Time: 09.30 am onwards

Venue: Seminar Hall, Electrical Engineering Department



Dr S S Jadhao
Chief Coordinator (R and D Cell)



Dr S B Somani
Principal and Chairperson of RAC

Copy to:

- 1) Dr. Mrs A.U. Jawadekar, Member RAC and Subject Expert for Evaluation
- 2) Dr S R Paraskar, Supervisor of Mr. Ravishankar Shaligram Kankale
- 3) Head, Electrical Engineering. Department for information and circulation amongst faculties and PhD Scholars
- 4) All HODs for information and circulation amongst faculties and PhD Scholars
- 5) Mr A B Wagh, for publication on website